

<b>Notice of References Cited</b>	Application/Control No. 10/619,884	Applicant(s)/Patent Under Reexamination MALDEI ET AL.	
	Examiner Brook Kebede	Art Unit 2823	Page 1 of 1

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X	C	US-5,858,837	01-1999	Sakoh et al.	438/255
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**NON-PATENT DOCUMENTS**

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	U	Wolf et al., Silicon Processing for The VLSI Era, Volume 1: Process Technology, Pp. 184, (1986).
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.